

Sheet: Input & Clock

File: Input_Clock V2.sch

Sheet: Counter & Decoder

File: Counter V2.sch

Sheet: Gate output

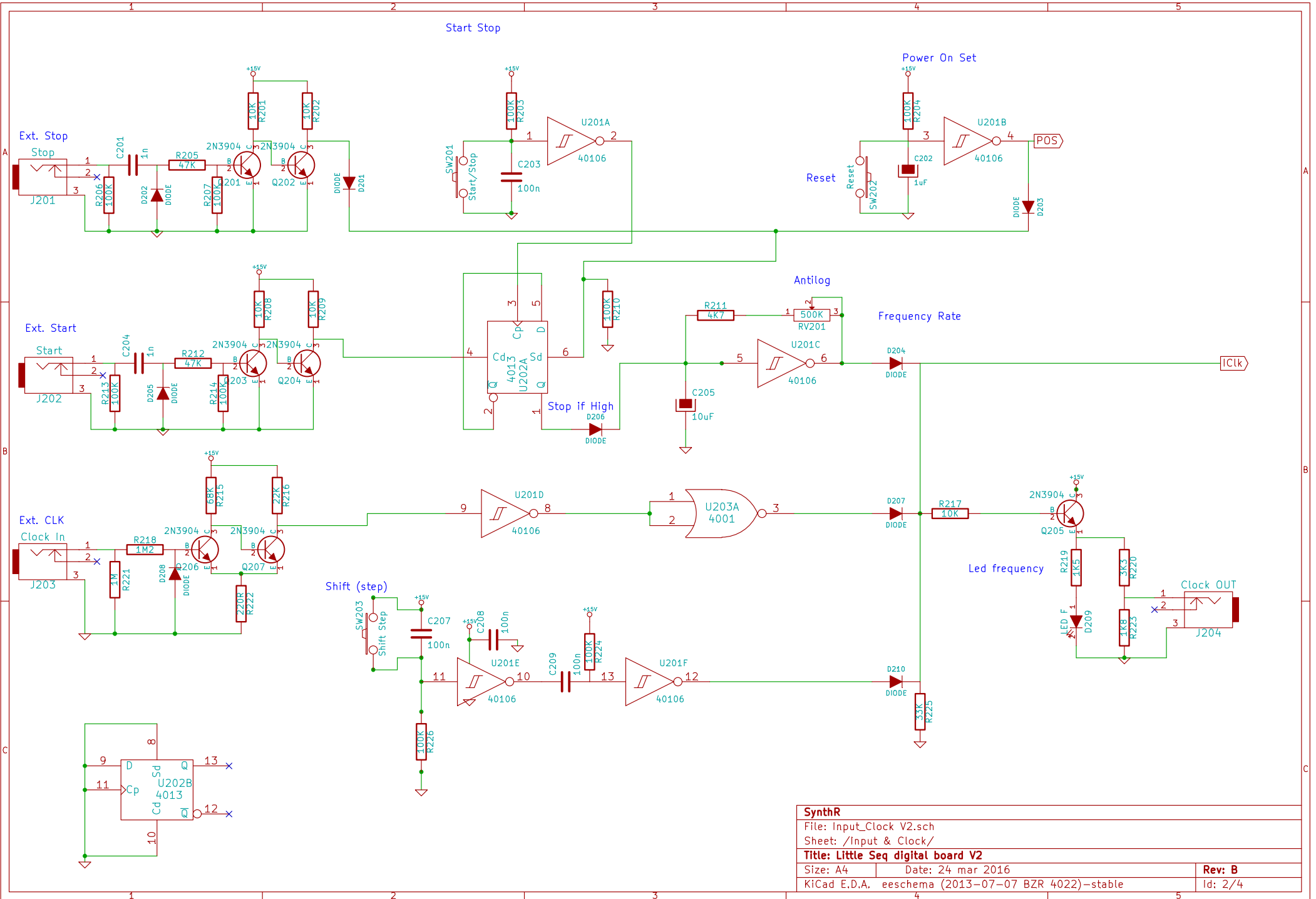
File: Gate output V2.sch

Features :

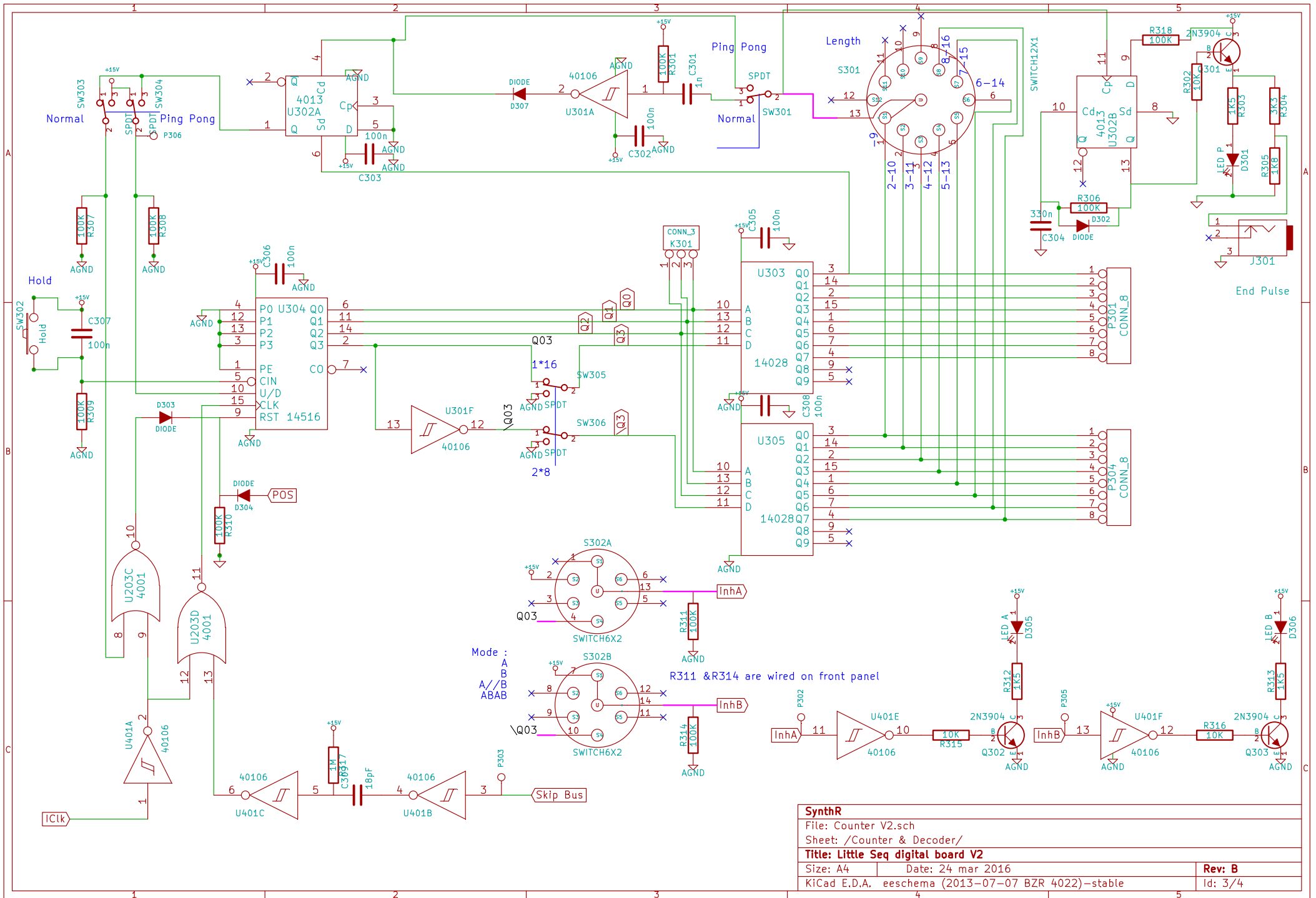
- 2*8 or 1*16 steps
- Reset at any length
- Normal, skip, off on each step
- Normal up or Ping Pong mode
- Voltage Control output from 1 to 5 octaves
- A output, B output, A and B in parallel, A B A mode
- Gate A and B
- Trigger A and B
- Internal clock (0,2 Hz to 42 Hz)
- Shift, Reset, Hold, Start/Stop
- External clock
- External start
- External stop
- Clock out (internal or external)
- Pulse end length (with Led)

Wires in magenta color are on Front Panel

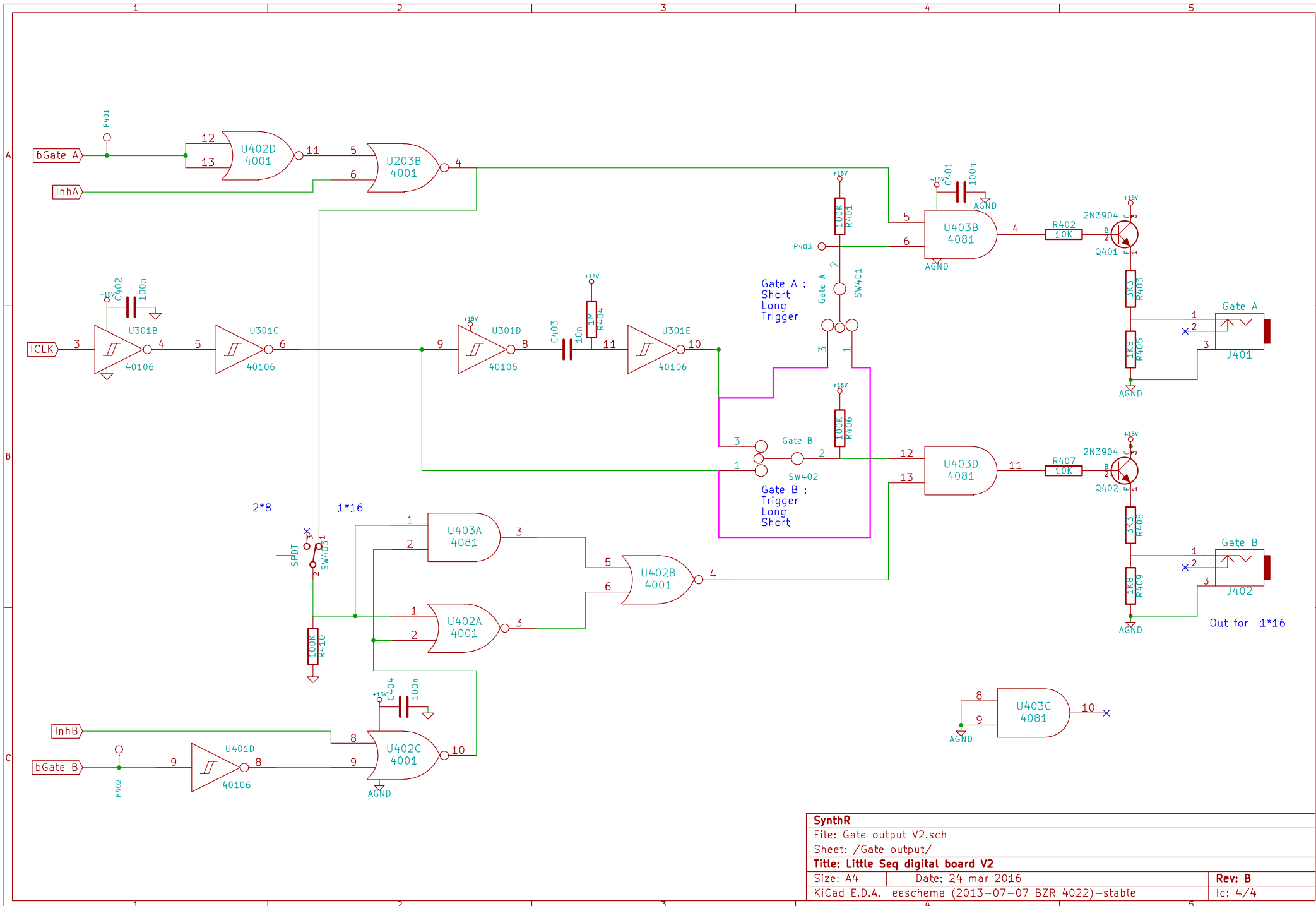
SynthR		
File: Little Seq V2 digital board.sch		
Sheet: /		
Title: Little Seq digital board V2		
Size: A4	Date: 24 mar 2016	Rev: B
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 1/4



SynthR		
File: Input_Clock V2.sch		
Sheet: /Input & Clock/		
Title: Little Seq digital board V2		
Size: A4	Date: 24 mar 2016	Rev: B
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable		Id: 2/4



SynthR	
File: Counter V2.sch	
Sheet: /Counter & Decoder/	
Title: Little Seq digital board V2	
Size: A4	Date: 24 mar 2016
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable	Rev: B
	Id: 3/4



SynthR		
File: Gate output V2.sch		
Sheet: /Gate output/		
Title: Little Seq digital board V2		
Size: A4	Date: 24 mar 2016	Rev: B
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable		Id: 4/4